We claim:

1.   A method for accessing a plurality of memory compiler units, the method comprising:

prompting, via a multi-compiler interface, for a selection of a first memory compiler unit from a plurality of memory compiler units;

remotely linking to the selected first memory compiler unit; and

generating a combination datasheet comprising a plurality of memory instances.

2.   The method of claim 1 further comprising:

prompting, via the multi-compiler interface, for a selection of a second memory compiler unit from the plurality of memory compiler units;

remotely linking to the selected second memory compiler unit; and

generating the combination datasheet comprising a plurality of memory instances created by both the first and second memory compiler units.

3.   The method of claim 2 further comprising:

displaying the combination datasheet on a web page.

4.   The method of claim 1 wherein the plurality of memory compiler units are provided on separate servers.

5.   The method of claim 1 further comprising providing the plurality of memory compiler units on at least two separate servers.

6.   The method of claim 1 further comprising providing the multi-compiler interface and the plurality of memory compiler units on at least two separate servers.

7.   The method of claim 1 wherein the prompting comprises:

     requesting a user to select a link to the first memory compiler

unit.

8.   The method of claim 1 wherein remotely linking to the
selected first memory compiler unit comprises:

     displaying a web page associated with the selected first

memory compiler unit.

9.   The method of claim 1 wherein remotely linking to the
selected first memory compiler unit comprises:

     soliciting inputs to one or more parameters through a web-

based screen display.

10.   The method of claim 1 wherein remotely linking to the
selected first memory compiler unit comprises:

     calculating memory instance ratios based on the inputs and

displaying the memory instance ratios on a web page.

11.   The method of claim 1 wherein remotely linking to the
selected first memory compiler unit comprises:

     creating a footprint based on the inputs and displaying the

footprint on a web page.

12.   The method of claim 1 wherein remotely linking to the
selected first memory compiler unit comprises:

     storing the design files on a FTP server.

13.   A computer readable medium comprising a plurality of
instructions for execution by at least one computer processor, wherein the
instructions are for:

        prompting a user to select a memory compiler unit;

        linking to a selected memory compiler unit; and

        generating a combination datasheet comprising a plurality of
memory instances wherein at least two of the plurality of memory instances
are created by different memory compiler units.

14.   The computer readable medium of claim 12 further
comprising providing a plurality of memory compiler units accessible
through a multi-compiler interface.

15.   The computer readable medium of claim 12 further
comprising displaying the combination datasheet on a web page.

16.   The computer readable medium of claim 12 wherein linking to
the selected memory compiler unit comprises:

        soliciting inputs to one or more parameters through a web
page.

17.   The computer readable medium of claim 12 further
comprising displaying memory instance ratios on a web page.

18.   The method of claim 12 further comprising displaying a
footprint on a web page.

19.   A system for providing a combination datasheet to a remote computer, the system comprising a plurality of memory compiler units wherein each memory compiler unit comprises a program for assisting a multi-compiler interface to generate a combination datasheet wherein the combination datasheet comprises memory instances created by at least two of the plurality of memory compiler units.

20.   The system of claim 19 wherein the plurality of memory compilers reside on at least two servers.